

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Sheredy, Joseph

Group Art Unit: Not Yet  
Assigned

Serial No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: Herewith

Title: Method and Apparatus for Testing a System on a Chip (SOC) Integrated  
Circuit Comprising a Hard Disk Controller and Read Channel**REQUEST AND CERTIFICATION  
UNDER 35 U.S.C. 122(b)(2)(B)(i)**Commissioner for Patents  
Washington, D.C. 20231

Sir:

I hereby certify that the invention disclosed in the attached application has not been and is not intended, at this time, to be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

**I hereby request that the attached application not be published under 35 U.S.C. 122(b).**

Applicant reserves the right to subsequently file an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing. At that time, Applicant intends to notify the United

States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application.

Respectfully submitted,



1/9/02

Eric B. Janofsky  
Attorney for Applicant  
Registration No. 30,759

Date: